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## AMENDMENTS TO THE CLAIMS

## Listing of the claims:

Following is a listing of all claims in the present application, which listing supersedes all previously presented claims:

- 1. (Canceled)
- 2. (Canceled)
- 3. (Original) A semiconductor integrated circuit device comprising:
- a power transistor that feeds a large current to a load;

a first transistor of which a second electrode and a control electrode are connected respectively to a second electrode and a control electrode of the power transistor;

a second transistor of which a second electrode is connected to a first electrode of the first transistor; and

a third transistor of which a control electrode is connected to a first electrode of the power transistor and of which a second electrode is connected to a control electrode of the second transistor,

wherein the second transistor outputs, at a first electrode thereof, a current signal proportional to a current flowing through the power transistor.

(Currently Amended) A semiconductor integrated circuit device as 4. claimed clamed in claim 3,

wherein a first electrode of the third transistor is connected to the second electrodes of the power transistor and the first transistor, and a direct-current

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voltage is applied through a resistor to the second electrode of the third

transistor.

5. (Currently Amended) A semiconductor integrated circuit device as

claimed clamed in claim 3,

wherein the second and third transistors are transistors of opposite

polarities, and a potential difference appearing between the second electrode

and control electrode of the second transistor is substantially equal to a potential

difference appearing between the second electrode and control electrode of the

third transistor.

6. (Currently Amended) A semiconductor integrated circuit device as

claimed clamed in claim 5,

wherein the second transistor is a transistor of a same polarity as the first

transistor.

7. (Currently Amended) A semiconductor integrated circuit device as

claimed clamed in claim 6,

wherein a first electrode of the third transistor is connected to the second

electrodes of the power transistor and the first transistor, and a direct-current

voltage is applied through a resistor to the second electrode of the third

transistor.

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